

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,870,255 B1
DATED : March 22, 2005
INVENTOR(S) : Steven Teig et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14.

Line 60, "wherein said pre-designed circuit block is a less than the" should read
-- wherein said pre-designed circuit block is less than the --.

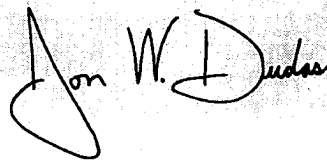
Column 16.

Line 17, "re-designed circuit block is independent of a layout for said" should read
-- pre-designed circuit block is independent of a layout for said --.

Line 49, "percent of the entire area of said first metal layer group" should read
-- percent of the entire area said metal layer in said first metal layer group. --.

Signed and Sealed this

Tenth Day of January, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS

Director of the United States Patent and Trademark Office